

CSIR-CENTRAL ELECTRONICS ENGINEERING RESEARCH INSTITUTE,

PILANI (RAJ.)

Advt. No. PF-07/2025

Dated: 29.07.2025

Online Interview/In Person Interview

CSIR-Central Electronics Engineering Research Institute (CSIR-CEERI) is a premier research Institute in the field of Electronics, set up under the Council of Scientific & Industrial Research (CSIR). The Institute is looking for qualified candidates for the following project staff positions purely temporary engagements for various ongoing projects in the Institute.

Candidates are requested to read “[Instruction To Apply](#)” before apply.
Online applications are invited from candidates for the Engagement of Project Staff.

Details of Vacancies & Eligibility

Post code	Project No. & Title	Area/Group	Name & Number of Positions	Essential Qualification as on the last date of receipt of online applications i.e. 04.08.2025	Age	Consolidated Emoluments Per month (fixed)
3305	GAP-3342 & Design and development of sealed and plasma assisted vacuum tubes for mm-wave absorption	High Power Microwave Systems Group	JRF & 01	B.E./ B.Tech. (EEE/ Electronics/EE/ECE/ Electronics & Instrumentation/ Microelectronics/ Mechanical/Applied Physics/ Nanotechnology/ Instrumentation) or equivalent OR M.Sc. (Electronics/ Physics/ Applied Electronics/E&I) or equivalent	35 Years	Rs. 37000/- +HRA
3306	GAP-3336 & High-power and Portable Sheet-beam Driven Sub-THz Source for Wireless High-Data-Rate Communication	High Power Microwave Systems Group	JRF & 02 NOTE: Joining Letter will be issued after receiving fund only.	B.E./ B.Tech. (EEE/ Electronics/EE/ECE/ Electronics & Instrumentation/ Microelectronics/ Mechanical/Applied Physics/ Nanotechnology/ Instrumentation) or equivalent OR M.Sc. (Electronics/ Physics/ Applied Electronics/E&I) or equivalent	35 Years	Rs. 31000/- UGC NET or GATE+HRA or Rs. 25000/- +HRA
3307	GAP-6519 & Development of Terahertz MEMS switches for 6G applications	Semiconductor Process Technology Group	JRF & 01	B.E./ B.Tech/ M.Sc (Electronics/ Physics/ ECE/ EEE/ Electronics & Instrumentation/ Electrical Engg./ Electronics & Telecommunication/ Microwave/mechatronics/MEMS) or equivalent. + Scholars who are selected through National Eligibility tests- CSIR-UGC NET, GATE Or The selection process through National level examinations conducted by central Government Departments and their Agencies	35 Years	Rs. 37000/- +HRA

				such as DST, DBT, DAE, DRDO, MoE, ICAR, ICMR, IIT, IISc, IISER, NISER etc		
3308	IHP202408 & Development of an AI-Integrated Flexible Endoscope for Colonoscopy Applications	AITG	PA-I & 01 and PA-II & 01	SSC/ 10 TH +ITI or Diploma in Engineering / Technology	35 Years	Rs. 18000/- +HRA or Rs. 20000/- +HRA
3309	MMP015203 & CSIR Mission Mode Project on 'CSIR League for AI-based Industrial Manifestation (CLAIM)' Task-2: Learned Lensless Camera System using Accelerated Inverse Computational Imaging Task-9: Baseline profiling and ML-based analysis for cognitive differentiation of alertness from a state of fatigue/drowsiness through physiological and neurocognitive profiling	Advanced Information Technologies Group	PAT-I/II & 01 and Senior-PAT & 01	<p>Project Associate –I (PAT-I) Essential Qualification: B.E./B.Tech in Electronics / Electronics and Instrumentations / Electronics and Communication Engineering / Electronics and Communications / Electronics and Electrical Engineering (EEE) or Equivalent</p> <p>Project Associate –II (PAT-II) Essential Qualification: B.E./B.Tech in Electronics / Electronics and Instrumentations / Electronics and Communication Engineering / Electronics and Communications / Electronics and Electrical Engineering (EEE) or Equivalent AND Two Years of Experience in R&D in Industrial and Academic Institutions or Science and Technology Organisations and Scientific Activities and Services in the Embedded System Development. OR</p> <p>(II) M.E./M.Tech./M.S. in Electronics and Instrumentations / Advanced Electronics Engineering / Electronics / Electronics and Communication Engineering/ Embedded Systems or Equivalent</p> <p>Senior Project Associate (Senior-PAT) Essential Qualification: (I) B.E./B.Tech in Electronics and Electrical Engineering (EEE)/Electronics and Instrumentations/ Electronics / Electronics and Communication Engineering/Computer Science & Engineering / Information Technology/Electronics and Communications or Equivalent OR M.Sc. in Computer Science or Equivalent AND Three Years of Experience in R&D in Industrial and Academic</p>	35 Years for PAT-I/II and 40 Years for Senior -PAT	As per CSIR-CEERI Guidelines For Project Associate-I (PAT-I) Rs. 31,000/- PM or Rs. 25,000/-PM as per CSIR Rules For Project Associate-II (PAT-II) Rs. 35,000/- PM or Rs. 28,000/-PM as per CSIR Rules For Senior Project Associate (Senior-PAT) Rs. 42,000/- PM as per CSIR Rules

				<p>Institutions or Science and Technology Organisations and Scientific Activities and Services in the areas of Software Development, IT, AI, Machine Learning Algorithms and Deep Learning Algorithms (with domain knowledge of Python).</p> <p>OR</p> <p>(II) M.E./M.Tech./M.S. in Electronics and Electrical Engineering (EEE)/Electronics and Instrumentations / Advanced Electronics Engineering / Electronics / Electronics and Communication Engineering/Computer Science & Engineering/ Embedded Systems or Equivalent OR Ph.D. (Science/Engineering/Technology) in the areas of AI, Machine Learning Algorithms, and Deep Learning Algorithms.</p> <p>AND</p> <p>Two Years of Experience in R&D in Industrial and Academic Institutions or Science and Technology Organisations and Scientific Activities and Services in the areas of Software Development, IT, AI, Machine Learning Algorithms and Deep Learning Algorithms (with domain knowledge of Python).</p>		
3310	HCP002502 & CSIR Integrated Skill Initiative phase-III	Skill Development Unit (CSIR-CEERI Jaipur Campus)	PAT-I & 01	<p>B.E/ B.Tech / M.Sc. (Electronics/ ECE/ EEE / Electronics & Instrumentation/ Electrical Engg. / Electronics & Telecommunication / Microwave/ mechatronics / MEMS/ Physics) or equivalent</p> <p>Desirable: Working Knowledge of embedded system, Knowledge of PCB design. Microcontroller Programming. Programming skills in Arduino. Knowledge of Circuit design. PCB Design.</p>	35 Years	Rs. 31000/- +HRA(for GATE) or Rs. 25000/- +HRA
3311	HCP002502 & CSIR Integrated Skill Initiative Phase-III	Skill Development Unit (CSIR-CEERI Jaipur Campus)	Project Assistant-II & 02	<p>B.sc/ 3 Years Diploma in Engineering & Technology(ECE/ EE/ Physics/ Electronics/ Material Science) or equivalent</p> <p>Desirable: Working knowledge of embedded system. Knowledge of PCB design. Programming skills in Arduino. Knowledge of circuit design.</p>	35 Years	Rs. 20000/- +HRA
3312	GAP-6109 & Ultrasonic-based Solar powered monitoring and	Industrial Automation Group	PAT-I & 01	B.E/ B. Tech/ M.Sc./ (ECE/ EEE/ EE/ Instrumentation / Electrical / Electronics/ Power Engineering / Renewable Energy) or equivalent.	35 Years	Rs. 31000/- +HRA or Rs. 25000/- +HRA

	control of Harmful Algal bloom			Desirable: Working experience and domain knowledge of areas of power electronics or circuit design or renewable systems.		
3313	HCP-0101 & JIGYASA	PME (Project Mangement and Evaluation Unit)	PAT-I/II & 01 Though the requirements are for PAT2, the eligibility criteria are kept for PAT1. If suitable candidate from PAT2 requirements are not found, a suitable candidate from the PAT1 pool will be considered for the position and will be paid at the PAT1 level only.	<p>PAT1: B.Tech/ BE in Computer Science/ IT/ Electronics, Masters in Computer Science/ Electronics, or equivalent.</p> <p>PAT2: B.Tech/ BE in Computer Science/ IT/ Electronics, Masters in Computer Science/ Electronics, or equivalent AND 2 years of experience.</p> <p>Preferable: 1) Experience in Programming, Animation, Database creation ARVR, Front End Design, Microcontroller based App Development and related virtual/ algorithm development.</p> <p>2) M.Tech in relevant stream or GATE/NET</p>	35 Years	<p>PAT1: 25000/- + HRA as per rule</p> <p>PAT2: 28000/- + HRA as per rule</p> <p><i>*GATE/ CSIR-NET rules will apply as per the CSIR Guidelines, and the stipend will be paid accordingly</i></p>
3314	GAP-3252 & Design, Fabrication and packaging of 650V pGaN E-mode HEMT for Power Switching Applications.	Semiconductor Process Technology Group	SRF & 01	<p>M.Sc. (Electronics/ Physics/ Applied Physics/ Applied Electronics/ E&I) degree holders with two years research experience.</p> <p>&</p> <p>M.E./ M.Tech/ in (EEE/ Electronics/ EE/ ECE / Electronics & Instrumentation / Compound-Semiconductor/ Microelectronics/ Applied Physics/ Nanotechnology/ Electronics/ Microelectronics/ VLSI Design).</p>	35 Years	Rs. 42000/- +HRA

*Candidates with above qualification without experience can also apply they shall be eligible for the position of PAT-I

For PAT – I:

(i) ₹31,000/- + HRA to Scholars who are selected through-

- National Eligibility Test- CSIR-UGC NET including lectureship (Assistant Professorship) or GATE or
- A selection process through National level examinations conducted by Central Government Departments and their Agencies and Institutions such as DST, DBT, DAE, DOS, DRDO, MHRD, ICAR, ICMR, IIT, IISC and IISER etc.

(ii) ₹25,000/- + HRA for others who do not fall under (i) above.

* For PAT – II:

(i) ₹35,000/- + HRA to Scholars who are selected through-

- National Eligibility Test- CSIR-UGC NET including lectureship (Assistant Professorship) or GATE or
- A selection process through National level examinations conducted by Central Government Departments and their

- Agencies and Institutions such as DST, DBT, DAE, DOS, DRDO, MHRD, ICAR, ICMR, IIT, IISC and IISER etc.
- (ii) ₹28,000/- + HRA for others who do not fall under (i) above

For AcSIR Ph.D. (Engineering/IDDP) Students

- 1. Those who does not have any fellowship can apply only when their research domain of Ph.D. is aligned with the research area of that project. Prior to applying, the Ph.D. student needs to have the permission of the Supervisor(s), PI of the project and approval from the Director. Scanned copy of the same should be uploaded on the portal.*
- 2. Those who are having fellowship, they are not eligible to apply.*

Interested candidates fulfilling the above qualifications must apply through online portal available on CSIR-CEERI website. A print out of the Application Form must be taken by the candidate after successfully submission of the form. The shortlisted candidates will be informed through 'E-mail/Institute website' along with the date and time for interview.

Candidates are requested to frequently browse the Institute website. Shortlisted candidates will have to appear for the interviews through "Microsoft Teams" (MS Teams). Candidates will be allowed as a Guest in MS Teams, for which the link will be send to your registered Email.

The selected candidates are expected to join immediately upon selection and bring with them all ORIGINAL TESTIMONIALS including application form, mark sheets & certificates and also a passport size photograph at the time of joining, failing which their engagement can be withdrawn.

For Recruitment queries: Sh. Rajan Tirkey Section Officer CSIR-CEERI, Pilani Email: recruitment@ceeri.res.in Phone:01596-252275	For Technical queries: Sh. Sanjeev Kumar Advanced Information Technologies Group CSIR-CEERI, Pilani Email: sanjeevkumar@ceeri.res.in Phone:01596-252282
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Other Conditions:

- Candidates who have already served CSIR-CEERI or any other lab/ Institute of CSIR as Project Staff such as Project Assistant/ PAT-I/II/ JRF (in contract R&D Projects)/ SRF (in contract R&D Projects)/ Research Associate/ PAT-I/II etc. for a total period of 6 years or more are not eligible for these engagements. The candidates who have served for a period less than 6 years will have tenure up to remaining period till completion of Six years.
- Tenure of Engagement: Six months initially or co-terminus with the present project or till such time the job to be performed by Project Staff in the project exists, whichever is earlier. The tenure may be extended in steps of six months for the duration of Project based on satisfactory performance. The total tenure as Research Associate, Project Assistant, PAT-I/II, JRF/SRF etc. shall not exceed 6 years in any case. The total tenure of Six years shall be calculated as per periods spent on one Project and/ or different Projects taken together in CSIR-CEERI, Pilani and/or any other Lab./Instt. of CSIR as RA, Project Assistant, PAT-I/II etc. or as any other designation of equal status.
- All applicants must fulfil the essential qualification and other conditions stipulated in the advertisement as on the last date of receipt of the online applications. They are advised to satisfy themselves before applying that they possess at least the essential qualifications laid down as on the last date of receipt of online application.**
- The date for determining the upper age limit, essential educational qualification and/or experience shall be the

last date of submission of online application.

5. The engagement of Scientific Administrative Assistant/Project Assistant, PAT-I/II, JRF, SRF etc. will be made on behalf of the Sponsor of the Projects. These are not CSIR-CEERI/CSIR posts and will not confer any right on the incumbent to claim, implicit or explicit, on any post in CSIR-CEERI/CSIR.
6. Reservation: As regards reservation, if all things are equal, SC/ST/OBC/PWBD/EWS candidates may be given preference over General candidates so as to ensure their representation.
7. **Candidates shall upload scanned copies of following documents on Project Staff Recruitment Portal of CSIR-CEERI:**
 - a. Candidates shall upload scanned copies of following academic certificates while applying for project staff positions :-
 - i. 10th Standard
 - ii. 12th Standard
 - iii. Diploma Degree
 - iv. Graduation Degree (B.A./ B.Com./ BBA/ BCA/ B.Sc./ B.E./ B.Tech. etc.)
 - v. Post-Graduation (M.Sc./ M.E./ M.Tech./ MS etc.)

Educational Qualification Certificate as mentioned in the advertisement essential qualification/ Semester Mark sheet or Final Mark sheet is compulsory. Along with this, Degree Award Certificate or Provisional Award Certificate. Percentage or CGPA/ DGPA should be clearly displayed on any one of the certificates. If certificate doesn't have proper percentage/ CGPA/ DGPA/ Grade then the application may not be considered. The candidate must ensure to provide proper certificates to ensure their eligibility for the positions they are applying for.

- b. Date of Birth Proof (Any valid certificate having DOB such as 10th Standard, DOB Certificate, etc.)
 - c. Category certificate (in case of SC/ ST/ OBC/ PWBD/ EWS candidates)
 - d. NET/ GATE/ Any other equivalent exam qualification certificate (If applicable)
 - e. Experience certificate (Mandatory for the posts where experience is essential qualification)
 - f. Other certificates (If applicable)
8. If it is found at any stage of the recruitment process or thereafter that the candidates do not fulfil the eligibility criteria, their candidature shall be cancelled without assigning any reason. Further, the decision of CSIR-CEERI in regard to the selection process will be final and binding to all concerned. No correspondence in this regard will be entertained by the Institute.
 9. Received applications will be screened by duly constituted Standing Screening Committee. The Screening Committee may fix different set of criteria to short-list the candidates as per the project requirement. Screening Committee will scrutinize and shortlist all the applications received for the specified project. Shortlisted candidates will be called for online interview. In case of false information received through online application, the competent authority will cancel the candidature of the candidate and debar for attending the interview in future.
 10. Shortlisted candidates may be called for interview through E-mail intimating the **date & time, link/ reference of online mode, terms & conditions**, if any. The candidate will be allowed to join only after verification of original certificates/ documents and other information found correct.
 11. Based on the performance of the candidates and availability of suitable candidates, Selection Committee may also recommend Panels for future engagement in different projects as and when need arises. The engagement process will be complete after the joining of the candidates.
 12. CSIR-CEERI reserves the right to cancel or withdraw the Offer of Engagement in case of any discrepancy found, in the candidature of any empaneled candidate at any stage.

13. The number of positions may be increased or decreased as per the requirements.

14. Other Terms & Conditions will be governed as per guidelines issued by the funding agency/CSIR/CEERI for the engagement of above Project Staff as amended from time to time.

The last date & time for applying applications is 04.08.2025 till 05:00 P.M. Application received after last date will not be entertained.

**Sd/-
SECTION OFFICER**