

Recruitment of Junior Research Associate in the Department of Electronics Engineering, IIT (BHU) Varanasi. Project Code No (of Institute): R&D/ECE/CSTUP/25-26/41/temp

Applications are invited from Indian nationals for the post of Junior Research Assistant to work in a Council of Science & Technology, Uttar Pradesh (CST-UP) sponsored research project titled "Development of ultrathin ZnS nano wire based room temperature operated single photon detector using ML for quantum information applications" (PID: 4765), sanctioned up to THREE Years.

- 1. Name of the position: Junior Research Associate (JRA)
- 2. Number of positions: 01
- 3. Last date of Application: Applicants should apply within 21 days of this advertisement.
- 4. Emoluments (p.m.): Rs. 25000/- per month
- 5. Essential Qualification:
 - a) Only Indian citizens who are **residents of Uttar Pradesh** are eligible for Research Assistantships/Associateships in the research project funded by CST-UP.
 - **b)** B.E/B.Tech in Electronics and Communication Engineering or allied branches with First Class (≥60% marks or equivalent grade)

OR

M.Sc. in Electronics, Physics, or allied areas with First Class (≥60% marks or equivalent grade).

- c) Candidates possessing higher qualifications in the relevant discipline may also be considered.
- **d)** The upper Age Limit is **28 Years** on 1st April 2025 (relaxable as Per GoI Rules). All things being equal, SC/ST candidates will be preferred as per GOI rules.

6. Desirable Qualifications:

- a) Valid GATE qualification in Electronics & Communication Engineering, or NET in Electronic Science/Physics, or any other national-level eligibility examination in the relevant discipline.
- **b)** Strong inclination towards experimental work, including semiconductor fabrication processes, handling of laboratory instruments, and measurement systems is desirable.
- c) Consistently strong academic record with no backlogs at the time of application.
- **d)** Demonstrated research aptitude and genuine motivation to pursue a research career, with interest in continuing towards a Ph.D. in the relevant area.
- 7. Application Procedure: Make a single PDF file having the filled application form (attached) with all relevant qualification certificates, marksheets, experience certificate, age proof, caste certificate, and others. Mail the PDF file to the following email ID "apandey.ece@iitbhu.ac.in" with the subject line "Application for UPCST/JRA/ECE/AP".

Note: No hard copy is required. Applicant needs to show all the original documents at the time of the interview, if shortlisted, based on the submitted data in the application.

8. General Terms and Conditions:

- a) The position is purely temporary and is coterminous with the project.
- **b)** The PI has the discretion to restrict the number of candidates to be called for interview to a reasonable limit based on qualifications and experience higher than the minimum prescribed in the advertisement.
- c) Only shortlisted candidates will be informed to appear in the interview, and no other communications in this regard will be entertained.
- d) The candidate is expected to join immediately if selected.
- e) No TA/DA will be paid for attending the interview.
- f) Other relevant Guidelines of the funding agency and the GoI rules will be followed.

Please feel free to contact in case of any query:

Dr. Amritanshu Pandey (Principal Investigator), Associate Professor, Department of Electronics Engineering, Indian Institute of Technology (BHU) Varanasi, Varanasi - 221005 (U.P.) India.

Email ID: apandey.ece@iitbhu.ac.in

Application Form

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